Wristband System/Subsystem Schedule Preview:

The schedule lists all of the jobs of each the system, electronics, and manufacturing engineer, that have been outlined as their responsibility in the Work Break Down Schedule, based on the Work Breakdown Structure explained in the NASA Systems Engineering textbook. The future dates have been set to an ideal time to finish in order to urge engineers to work hard at their goals. As assignments are given by the president, a detailed set of Wristband Sub-system sub-tasks are input into the original sub-tasks set by the project manager's calendar to more clearly define the process of each task. It is important to clearly define the all system requirements, so these sub-task assignments are provided to them in documentation by the Wristband System Engineer. These assignments, as well as the documents submitted, are uploaded as an attachment to the task on the schedule program. If the tasks fail to be completed on by the deadline the task is marked with a red "at risk" flag next to it, to further assess the issue at the project group meeting.

System/Subsystem Level Tasks

	At Risk	Task Name	Start Date	End Date	Duration	% Complete	Predecessors
1		Need Help? Learn more about this template.					
2							
3		**Enter your deadline as start and end date:**	**01/20/16**	**05/04/16**	**76d**		
4		**Missions, Systems, and Test (Robin Yancey)**	**01/20/16**	**03/23/16**	**46d**	**51%**	
5		System Design	01/20/16	03/23/16	46d	72%	
6		Level 2 Requirements	02/03/16	02/24/16	16d	94%	
7		Research ECG to and System Engineering of the 2015 A-TeChToP	02/03/16	02/10/16	6d	100%	
8		Find out What went wrong and Research Methods to Fix the Problem	02/03/16	02/10/16	6d	100%	
9		Research EDA all Design/Methods for Sensors Wristbands which have been developed	02/10/16	02/24/16	11d	80%	
10		Define Important Requirements of Type for System	02/10/16	02/24/16	11d	100%	
11		Do Further Research to write Requirements which will help Guide Choices of Sensors and Materials for Electronics and Manufacturing	02/17/16	02/23/16	5d	100%	
12		Resource Report	02/17/16	02/23/16	5d	0.87	
13		Define System Resources & Make Data Sheets	02/17/16	02/18/16	2d	100%	
14		Check That Parts meet all System\Subsystem requirements (listed+unlisted)	02/21/16	02/22/16	2d	100%	
15		Discuss Parts to be Purchased with Subsystems	02/23/16	02/23/16	1d	70%	
16		Find Missing Data Sheet Information Based on Choices	02/23/16	02/23/16	1d	50%	
17		Interface Defintion	02/22/16	03/23/16	23d	44%	
18		Thoroughly Read MCU Data Sheet	02/22/16	02/23/16	2d	90%	
19		Thoroughly Read Data Sheets of External Electronic Parts	02/23/16	03/23/16	22d	40%	
20		System Block Diagram	02/24/16	02/25/16	2d	20%	
21		Using Interface Definitions, Simplify the System into Blocks	02/24/16	02/25/16	2d	20%	
22		Define Cable Tree	02/23/16	02/25/16	3d	30%	
23		Design Necessary Cable connections to Connect Interface Definitions	02/23/16	02/25/16	3d	30%	

	At Risk	Task Name	Start Date	End Date	Duration	% Complete	Predecessors
24		Grounding Strategy	01/20/16	02/24/16	26d	90%	
25		Intangibles	01/20/16	02/24/16	26d	70%	
26		Software	02/24/16	03/09/16	11d	10%	
30		System Test	02/24/16	03/09/16	11d	4%	
35		**Electronics and Control (Rose Leidenfrost)**	**02/10/16**	**03/16/16**	**26d**	**12%**	
36		Electronic Design	02/10/16	02/24/16	11d	13%	
37		Level 3 Subsystem Requirements	02/17/16	02/19/16	3d	30%	
38		Research Sensors Specified in the Level 2 Requirements and provide Explanations for any further sensor requirements	02/17/16	02/19/16	3d	30%	
39		Component Definitions	02/10/16	02/24/16	11d	10%	
40		Use research Knowledge to make good Choices for Commercially Sold Parts, and document trade off studies based on research and Level 2/Level 1 requirements	02/17/16	02/19/16	3d	70%	
41		Sensors	02/10/16	02/24/16	11d	0%	
42		Define EDA	02/10/16	02/24/16	11d	0%	
43		Define Accelerometer	02/17/16	02/24/16	6d	0%	
44		Power	02/17/16	02/24/16	6d	0%	
45		Define Battery	02/17/16	02/24/16	6d	0%	
46		Fritzing Diagram	02/24/16	02/24/16	1d	0%	
47		Capture Electrical Schematic	02/24/16	02/24/16	1d	0%	
48		Experiments	02/10/16	03/16/16	26d	29%	
52		Microcontroller	02/24/16	03/16/16	16d	0%	
57		Communications	02/10/16	03/09/16	21d	0%	
59		**Design and Manufacturing (Marena William)**	**02/03/16**	**05/04/16**	**66d**	**3%**	
60		Mechanical Design	02/03/16	03/09/16	26d	12%	
61		Level 3 Subsystem Requirements	02/10/16	02/26/16	13d	30%	
62		Research Materials following the Level 2 Requirements and provide Explanations for any further materials requirements	02/10/16	02/26/16	13d	30%	

	At Risk	Task Name	Start Date	End Date	Duration	% Complete
63		Design Watch Band	02/17/16	02/19/16	3d	40%
64		Use research Knowledge to make good Choices for Commercially Sold Materials, and document trade off studies based on research and Level 2/Level 1 requirements	02/17/16	02/19/16	3d	70%
65		Make a Mechanical Design of the Wrist Band to Post	02/17/16	02/19/16	3d	10%
66		Mechanical Interface Definition	02/03/16	02/23/16	15d	0%
67		CAD Software to Design Watch Chassis	02/17/16	02/24/16	6d	0%
68		Simulations	03/02/16	03/09/16	6d	0%
69		PCB	02/24/16	03/23/16	21d	0%
76		Component Manufacturing	03/02/16	03/16/16	11d	0%
80		Wiring Harness (The Cable Tree)	02/17/16	03/09/16	16d	0%
83		A-TeChToP Assembly	04/13/16	05/04/16	16d	0%